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Title of invention:	SEMICONDUCTOR MEMORY PREVENTING AN ELECTRIC SHORT CIRCUIT BETWEEN A WORD LINE AND A SEMICONDUCTOR SUBSTRATE, AND MANUFACTURING METHOD FOR THE SEMICONDUCTOR MEMORY		
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Number	Document Description	File Name	Message Digest	Part /.zip	(if appl.)
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Multipart Description/PDF files in .zip description				
Document Description	Start	End		
Amendment/Req. Reconsideration-After Non-Final Reject	1	1		
Claims	2	7		
Applicant Arguments/Remarks Made in an Amendment	8	8		

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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